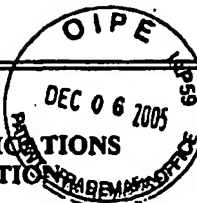


FORM PTO-1449 (Modified)



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**LIST OF PATENTS AND PUBLICATIONS
FOR APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

APPLICANT: Ben-David et al.

FILING DATE:

29-Oct-2003

GROUP:

2183

2191

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
MA	AA	6665790	12-16-2003	Glossner et al.			2000-02-29
MA	AB	6915411	7-5-2005	Moreno et al.			2002-07-18
MA	AC	2004-0078554	4-22-2004	Glossner et al.			2003-06-07

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

MA	AD	Preeti Ranjan Panda, Nikil D. Dutt, and Alexandru Nicolau. "Efficient utilization of scratch-pad memory in embedded processor applications", in European Design and Test Conf., March 1997						
MA	AE	Keith D. Cooper, Timothy J. Harvey. "Compiler controlled Memory", Eighth Int. Conf. on Architectural Support for Programming Languages and Operating Systems, pp. 100-104, Oct, 1998						
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EXAMINER D

MA

DATE CONSIDERED

2-6-2007

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.